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VOLENTINE FRANCOS PLLC

#7137 P.001/004

CERTIFICATE OF TRANSMISSION BY FACSIMILE (37 CFR 1.8)			Docket No.
Applicant(s): Makoto TERUI et al.			OKL597
Serial No. 10/717,934	Filing Date 11/21/2003	Examiner TBA	Group Art Unit 2812
Invention: METHOD FOR FABRICATING A SEMICONDUCTOR DEVICE HAVING A HEAT SPREADING LAYER			
<p>I hereby certify that this <u>REQUEST FOR CORRECTED FILING RECEIPT</u> <small>(Identify type of correspondence)</small></p> <p>is being facsimile transmitted to the United States Patent and Trademark Office (Fax. No. <u>703-746-9195</u>)</p> <p>on <u>APRIL 29, 2004</u> <small>(Date)</small></p> <div style="text-align: right;"><p><u>Adam C Volentine</u> <small>(Typed or Printed Name of Person Signing Certificate)</small></p><p><u>[Signature]</u> <small>(Signature)</small></p></div> <p>Note: Each paper must have its own certificate of mailing.</p>			

P18/REV01

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT APPLICATION of :
Makoto TERUI et al. :
Serial No. 10/717,934 : Group Art Unit 2812
Filed: November 21, 2003 :

METHOD FOR FABRICATING A SEMICONDUCTOR DEVICE HAVING A HEAT SPREADING LAYER

REQUEST FOR CORRECTED FILING RECEIPT

Official of Initial Patent Examination's
Filing Receipt Corrections
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Attached is a copy of the official filing receipt from the PTO in the above application for which issuance of a corrected filing receipt is respectfully requested.


Please correct the following data as follows:

Please change the first Applicant's name as follows:

Makoto TERUI

Respectfully submitted,

VOLENTINE FRANCOS, PLLC


Adam C. Volentine
Registration No. 33289

12200 Sunrise Valley Drive, Suite 150
Reston, Virginia 20191
Telephone No.: (703) 715-0870
Facsimile No. (703) 715-0877

Dated: April 29, 2004



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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/717,934	11/21/2003	2812	0.00	OKI.597	19	18	4

CONFIRMATION NO. 7542

VOLENTINE FRANCOS, P.L.L.C.
Suite 150
12200 Sunrise Valley Drive
Reston, VA 20191

FILING RECEIPT

**VOLENTINE FRANCOS
P.L.L.C.**

OC000000012046832

MAR 09 2004

Date Mailed: 03/08/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Makoto Terui Terui Makoto, Residence Not Provided;
Yasuo Tanaka, Residence Not Provided;
Takashi Noguchi, Residence Not Provided;

Domestic Priority data as claimed by applicant

Foreign Applications

JAPAN 2002-339332 11/22/2002

If Required, Foreign Filing License Granted: 03/08/2004

Projected Publication Date: To Be Determined - pending completion of Missing Parts

Non-Publication Request: No

Early Publication Request: No

Title

Method for fabricating a semiconductor device having a heat spreading layer

Preliminary Class

438

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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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